

REMARKS

Reconsideration of the application, as amended, is respectfully requested. Applicants also acknowledge with appreciation the Examiner's indication that claims 3-6 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

I. STATUS OF CLAIMS

Claims 1-10 are pending. Claims 1 and 10 have been amended herewith to more particularly point and distinctly claim that which applicants regard as their invention. In particular, claim 1 has been amended to further clarify that the thin film transistor array panel for a liquid crystal display includes a common wire formed with the same layer as the black matrix and including a common signal line for transmitting a common signal from external circuits. In addition, claim 10 has been amended to further clarify that the method for manufacturing a thin film transistor array panel includes forming a common wire including a common signal line for transmitting common signals from external circuits. Moreover, claim 3 has been canceled herewith without prejudice. New claims 36 and 37 has been added.

Support for the above amendments and new claims may be found throughout the specification as originally filed. It is respectfully submitted that no new matter has been added by virtue of this amendment.

II. 35 U.S.C. 102(e) and 35 U.S.C. 103(a) Rejections

(i) Claims 1 and 7-9 have been rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 5,299,041 in view of U.S. Patent Application No. US20010035919 A1, U.S. Patent No. 6,532,050 B1, U.S. Patent No. 5,995,174, U.S. Patent No. 5,986,725 A, U.S. Patent No. 5,978,058, U.S. Patent No. 5,977,563 A, U.S. Patent No. 5,926,242A, U.S. Patent No. 5,926,235A, U.S. Patent No. 5,923,390A, U.S. Patent No. 5,883,682A, U.S. Patent No. 5,852,488A, U.S. Patent No. 5,847,781A and U.S. Patent No. 5,818,550A.

(ii) Claims 2 and 10 have been rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 5,299,041 in view of U.S. Patent Application No. US20010035919 A1, U.S. Patent No. 6,532,050 B1, U.S. Patent No. 5,995,174, U.S. Patent No. 5,986,725 A, U.S. Patent No. 5,978,058, U.S. Patent No. 5,977,563 A, U.S. Patent No. 5,926,242A, U.S. Patent No. 5,926,235A, U.S. Patent No. 5,923,390A, U.S. Patent No. 5,883,682A, U.S. Patent No. 5,852,488A, U.S. Patent No. 5,847,781A and U.S. Patent No. 5,818,550A, as applied to claim 1 above and further in view of US 6,400,440B1.

(iii) Claims 2 and 9-10 have been rejected under 35 U.S.C. 103(a) as being unpatentable over US 6,532,050B1 in view of US 6,400,440B1.

(iv) Claims 1 and 7-8 have been rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent No. 6,532,050B1.

In order for a claim to be rendered unpatentable by the cited art, the cited art must either (i) anticipate the claim or otherwise (ii) render the claim obvious. A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference. (See MPEP 2133, *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987)). Alternatively, to establish prima facie obviousness of a claimed invention, all of the claim limitations must be taught or suggested by the cited reference or references. (See MPEP 2143.03; *In re Royka*, 490 F.2d 981, 180 USPQ 580 (CCPA 1974)).

However, all of the above-mentioned references which have been cited in the instant Office Action alone or in combination fail to teach or suggest all of the features recited in claims 1 and 10.

As noted above claim 1 has been amended to further clarify that the thin film transistor array panel for a liquid crystal display includes a common wire formed with the same layer as the black matrix and including a common signal line for transmitting a common signal from external circuits. In addition, claim 10 has been amended to further clarify that the method for

manufacturing a thin film transistor array panel includes forming a common wire including a common signal line for transmitting common signals from external circuits.

In particular, each of the above cited references alone or in combination at the very least fail to teach or suggest a thin film transistor array panel for a liquid crystal display which includes a common wire formed with the same layer as the black matrix and including a common signal line for transmitting a common signal from external circuits, as recited in claim 1. Furthermore, each of the above cited references alone or in combination at the very least fail to teach or suggest a method for manufacturing a thin film transistor array panel which includes forming a common wire including a common signal line for transmitting common signals from external circuits, as recited in claim 10.

Therefore, for at least the reasons set forth above, each of the above cited references in the instant Office Action alone or in combination fail to teach or suggest all of the features recited in claim 1 and 10. Consequently, claims 1 and 10 are patentable over each of the above cited references alone or combination and thus removal of the rejections to these claims is respectfully requested. Furthermore, as claims 2 and 4-9 depend from and incorporate all of the limitations of claim 1, removal of the rejections to these dependent claims is likewise requested. Moreover, as new claim 37 depends from and incorporate all of the limitations of claim 10, this dependent claim is likewise patentable over all of the above cited references alone or in combination.

It is further noted that in addition to the reasons given above, the rejections to claims 1, 2 and 4-10 should also be withdrawn because at the very least all of these rejections are defective. In particular, all of these rejections are defective because at the very least, cited reference, U.S. patent 6,532,050 B1 to Kim does not qualify as prior art with respect to any of the pending claims. Namely, the effective filing date of Kim for 35 U.S.C. 102(e) purposes is after foreign priority dates claimed by the present application to Korean Patent Application No. 99-62915 and Korean Patent Application No. 99-42108. Specifically, the effective 35 U.S.C. 102(e) filing date of Kim is July 7, 2000, whereas priority dates of the present application include December 27, 1999 (Korean Patent Application No. 99-62915) and September 30, 1999 (Korean Patent Application No. 99-42108). Thus, the Kim reference does not constitute prior art with respect to

the pending claims and cannot be relied upon in any of the above-mentioned rejections. Accordingly, all of the above claims rejections are legally deficient on their face and consequently must be withdrawn.

In further support of the above argument and pursuant to **MPEP 201.15**, an English translation of the certified copies of the above-mentioned Korean priority applications, together with a statement that the translation of the certified copy of the priority applications are accurate are being forwarded to the Examiner separately from this amendment, shortly.

New claim 36 is also patentable over all of the above cited references because these references at the very least fail to teach or suggest a thin film transistor array panel for a liquid crystal display which includes a common wire formed of the same layer as the black matrix and including a common signal line transmitting a common signal to a common electrode opposing the pixel electrode and common pads transmitting the common signal to the common signal line from external circuits and connected to the common signal line, as recited in this claim. Furthermore, claim 36 corresponds to prior claim 3 amended into independent format to include all of the limitations of prior claim 1. As noted above, the Examiner indicated that claim 3 would be allowable if rewritten in independent form to include all of the limitations of the base claim and any intervening claim.

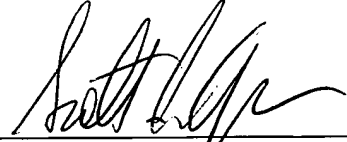
Thus, for at least the reasons set forth above, new claim 36 is patentable over all of the above cited references.

III. CONCLUSION:

In summary, applicants respectfully submit that the instant application is in condition for allowance. Early notice to that end is earnestly solicited.

If a telephone conference would be of assistance in furthering prosecution of the subject application, applicant requests that the undersigned be contacted at the number below.

Respectfully submitted,



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